

**TRANSMITTAL OF FORMAL DRAWINGS**

Docket No.

907A.0138.U1(US)

In Re: Application Of: Nelson et al.

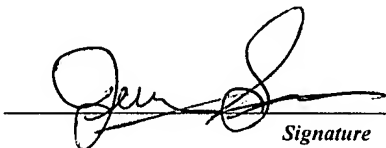
Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/690,898	10/22/2003	Bolourchi, Nader	29683	2611	6357

Invention: Parallel Fractional Interpolator With Data-Rate Clock Synchronization

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

10 sheets of formal drawing(s) for this application.

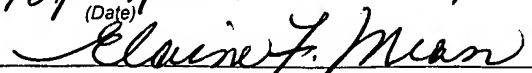
☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).
Signature

Jerry Stanton (Reg. No. 46,008)
Harrington & Smith, PC
4 Research Drive
Shelton, CT 06484-6212

Dated: Dec 27, 2007

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

12/27/07
(Date)



Signature of Person Mailing Correspondence

Elaine F. Mian

Typed or Printed Name of Person Mailing Correspondence